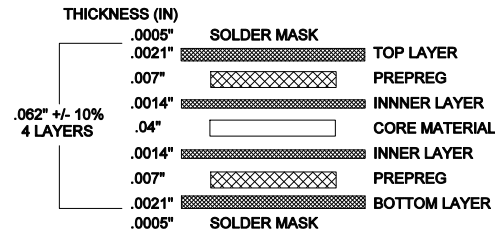


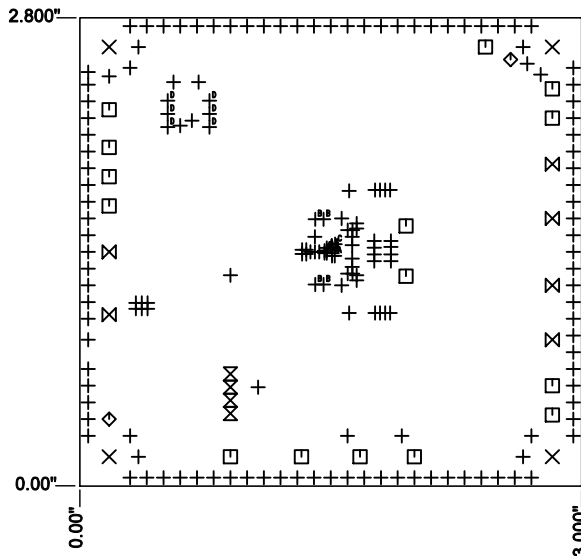
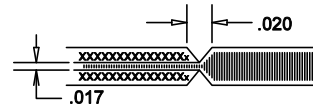
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	03	PRODUCTION	MM	03-22-21

LAYER STRUCTURE






NOTES: UNLESS OTHERWISE SPECIFIED

- FABRICATION: FABRICATION AND ACCEPTANCE TO MEET THE REQUIREMENTS OF IPC-A-600 AND IPC/ANSI-MLL950-C CLASS 2, AQL .25 GENERAL INSPECTION LEVEL II.
- MATERIAL: - EPOXY FIBERGLASS, NEMA GRADE FR-4
- FINISHED THICKNESS TO BE 0.062" +/- .005"
- TOTAL OF 4 LAYERS WITH 2 OUNCE Cu ON EXTERNAL LAYERS AND 1OZ Cu ON INTERNAL LAYERS.
- FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN 0.00" ARE PRIMARY DATUMS.
- DRILLING: - DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
- ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
- HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER.
- FINISH: - SMOBC USING LPI BOTH SIDES, COLOR GREEN. TENT BOTH SIDES.
- GOLD IMMERSION BOTH SIDES.
- FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.004	10	+A	YES	+/-0.002"
0.006	4	+B	YES	+/-0.003"
0.008	1	+C	YES	+/-0.003"
0.012	159	+D	YES	+/-0.003"
0.035	4	+E	YES	+/-0.003"
0.04	6	+F	YES	+/-0.003"
0.065	15	+G	YES	+/-0.003"
0.07	2	+H	NO	+/-0.003"
0.095	6	+I	YES	+/-0.003"
0.19	4	+J	YES	+/-0.003"

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.X0" = ±0.01" 0.X00" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION 	APPROVALS		  FOR ADI CUSTOMER USE ONLY TITLE: FABRICATION DRAWING 3.3V VIN, DUAL 2A, 2MHz SYNCHRONOUS STEP-DOWN REGULATORS WITH 1.2V AND 1.8V OUTPUTS, IN A 74mm² SOLUTION SIZE N/A IC NO. LTC3315A DEMO CIRCUIT 3123A REV 03
	PCB DES	NC	
	APP ENG	MM	
	SCALE = NONE		
		SHT 1 OF 1	